

SINGLE-SUPPLY DUAL OPERATIONAL AMPLIFIER

■ GENERAL DESCRIPTION

The NJM2904 consists of two independent, high gain, internally frequency compensated operation amplifiers which were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.

Application areas include transducer amplifiers, DC gain blocks, and all the conventional op amp circuits which now can be more easily implemented in single power supply systems. For example, the NJM2904 can be directly operated off of the standard +5V power supply voltage which is used in digital systems and will easily provide the required interface electronics without requiring the additional $\pm 15V$ power supplies.

■ FEATURES

- Single Supply
- Operating Voltage $(+5V \sim +32V)$
- Low Operating Current $(0.7mA \text{ typ.})$
- Slew Rate $(0.5V/\mu s \text{ typ.})$
- Bipolar Technology
- Package Outline DTPK, DMPK, SIPR, SSOP8

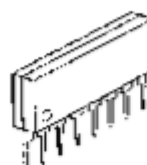
■ PACKAGE OUTLINE



NJM2904D



NJM2904M

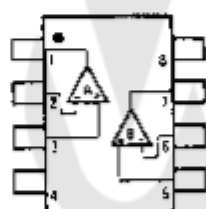


NJM2904L



NJM2904V

■ PIN CONFIGURATION



NJM2904D
NJM2904M
NJM2904V

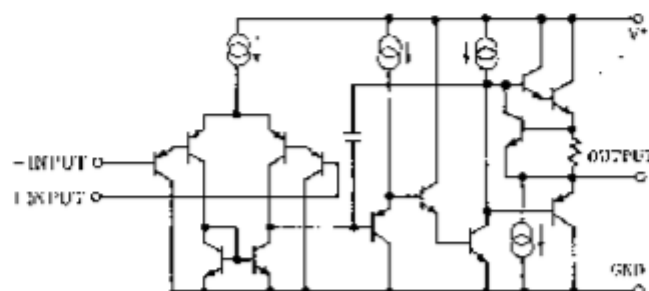
PIN FUNCTION

1. A OUTPUT
2. A-INPUT
3. A+INPUT
4. GND
5. B+INPUT
6. B-INPUT
7. B OUTPUT
8. V-



NJM2904L

■ EQUIVALENT CIRCUIT (1/2 Shown)



■ ABSOLUTE MAXIMUM RATINGS

($T_a=25^{\circ}\text{C}$)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	$V^+(V^-/V^-)$	32 for ± 16	V
Differential Input Voltage	V_{in}	32	V
Input Voltage	V_{ic}	$-0.3 \sim +32$	V
Power Dissipation	P_D	(DIP8) 500	mW
		(DMP8) 300	mW
		(SSOP8) 300	mW
		(SIP8) 800	mW
Operating Temperature Range	T_{op}	$-40 \sim +85$	$^{\circ}\text{C}$
Storage Temperature Range	T_{stg}	$-50 \sim +125$	$^{\circ}\text{C}$

■ ELECTRICAL CHARACTERISTICS

($T_a=25^{\circ}\text{C}$ $V^+=5\text{V}$)

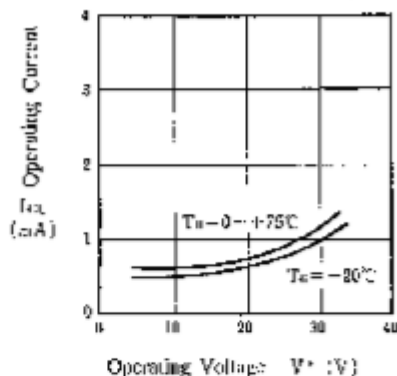
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V_{io}	$R_S=0\Omega$	—	2	7	mV
Input Offset Current	I_{io}		—	5	50	nA
Input Bias Current	I_b		—	25	250	nA
Large Signal Voltage Gain	A_v	$R_L \geq 2k\Omega$	—	160	—	dB
Maximum Output Voltage Swing	V_{om}	$R_L=2k\Omega$	3.5	—	—	V
Input Common Mode Voltage Range	V_{icm}		0~3.5	—	—	V
Common Mode Rejection Ratio	CMR		—	85	—	dB
Supply Voltage Rejection Ratio	SVR		—	100	—	dB
Output Source Current	I_{source}	$V_{is}=1\text{V}$, $V_{os}=0\text{V}$	20	30	—	mA
Output Sink Current	I_{sink}	$V_{in}=0\text{V}$, $V_{os}=1\text{V}$	8	20	—	mA
Channel Separation	CS	$f=1k \sim 20k\text{Hz}$, Input Referred	—	130	—	dB
Operating Current	I_{cc}	$R_L=\infty$	—	0.7	1.2	mA
Slew Rate	SR	$V^+/V^- = \pm 15\text{V}$	—	0.5	—	V/ μs
Unity Gain Bandwidth	f_t	$V^+/V^- = \pm 15\text{V}$	—	0.2	—	MHz

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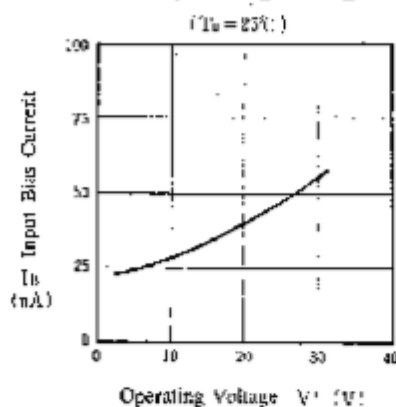
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■ TYPICAL CHARACTERISTICS

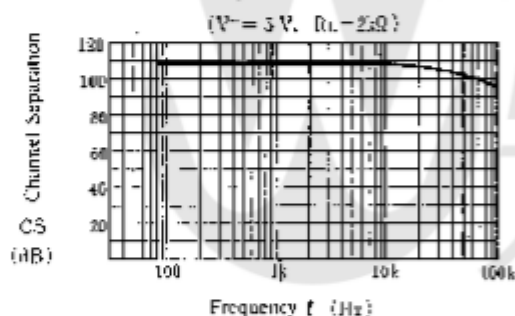
Operating Current
vs. Operating Voltage



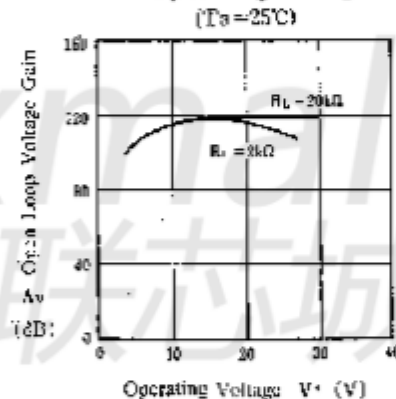
Input Bias Current
vs. Operating Voltage



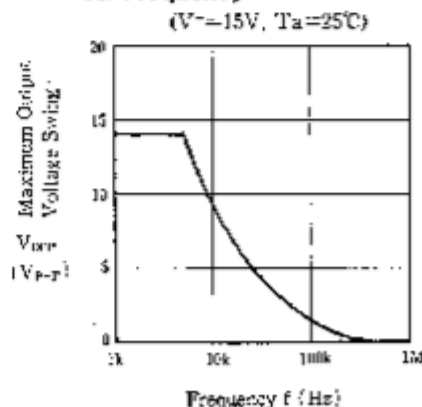
Channel Separation vs. Frequency



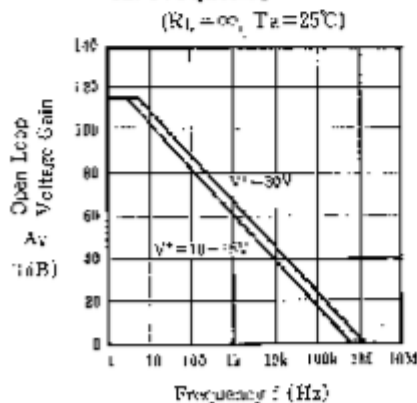
Voltage Gain
vs. Operating Voltage



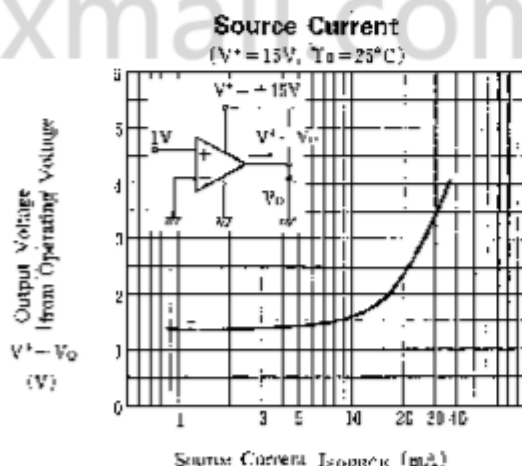
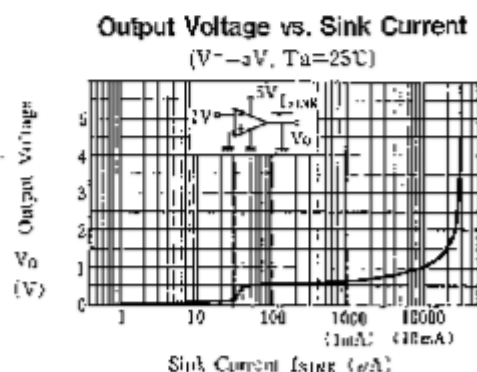
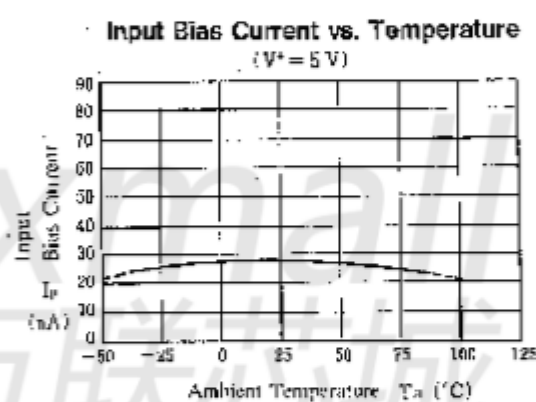
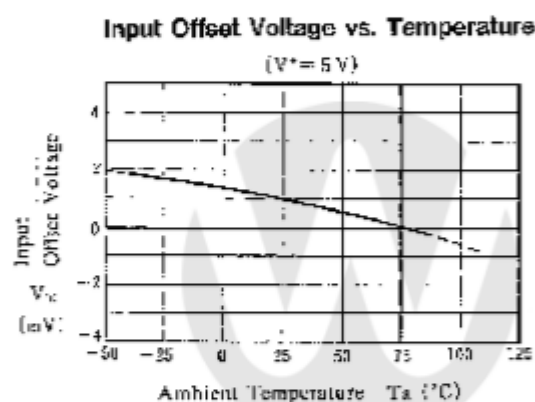
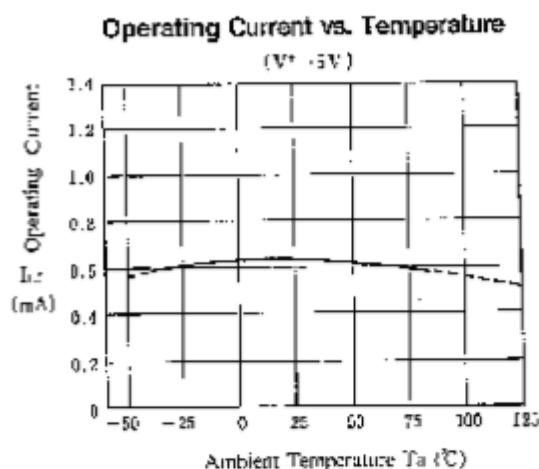
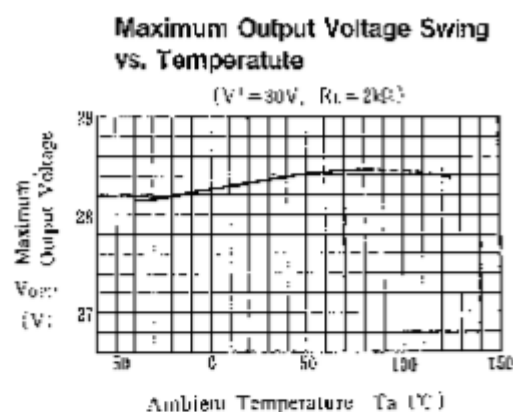
Maximum Output Voltage Swing
vs. Frequency



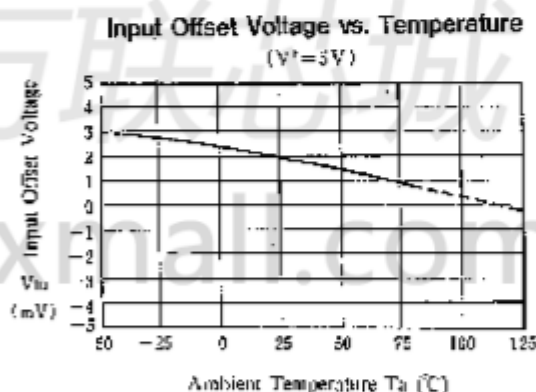
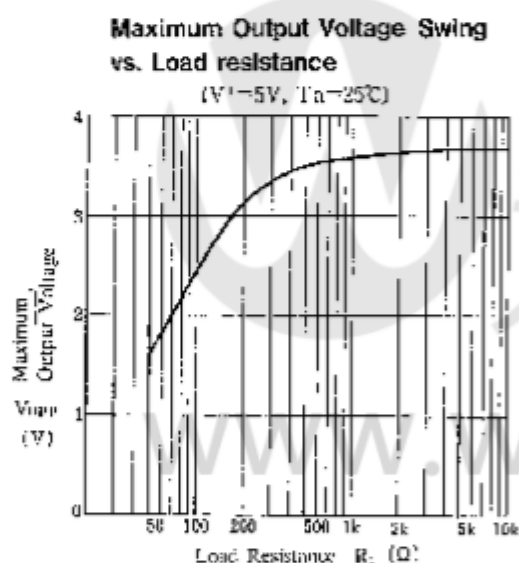
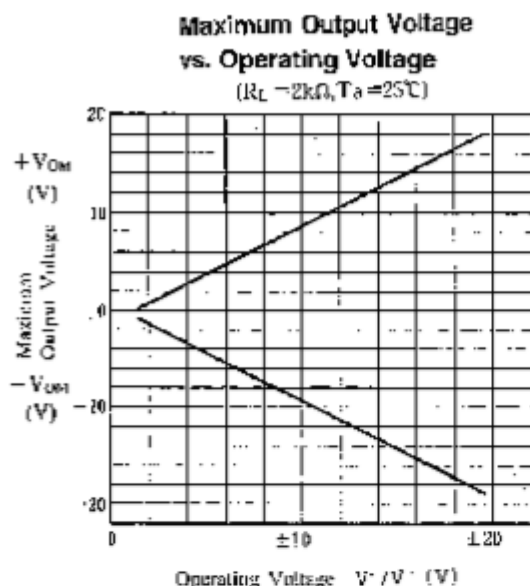
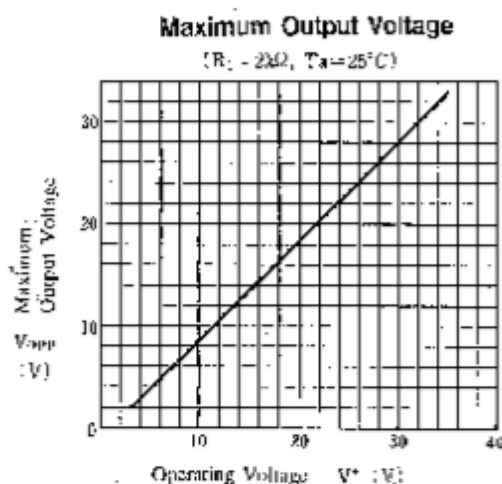
Open Loop Voltage Gain
vs. Frequency



■ TYPICAL CHARACTERISTICS

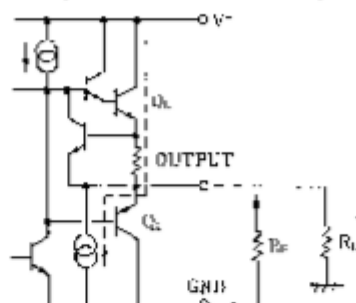


■ TYPICAL CHARACTERISTICS



APPLICATION

- Improvement of Cross-over Distortion
Equivalent circuit at the output stage

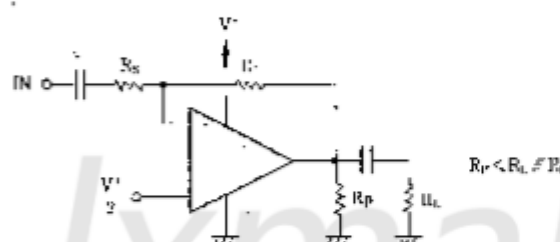
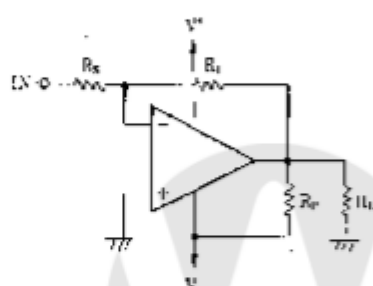


NJM2904, in its static state (No in and output condition) when design, Q_1 being biased by constant current (break down beam) yet, Q_1 stays OFF.

While using with both power source mode, the cross-over distortion might occur instantly when Q_1 ON.

There might be cases when application for amplifier of audio signals, not only distortion but also the apparent frequency bandwidth being narrowed remarkably.

It is advisable especially when using both power source mode, constantly to use with higher current on Q_1 than the load current (including feedback current), and then connect the pull-down resistor R_P at the part between output and GND pins.



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备忘录



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