

SINGLE-SUPPLY LOW POWER DUAL OPERATIONAL AMPLIFIER

■ GENERAL DESCRIPTION

The NJM2132 is a general purpose single supply low power dual operational amplifier.

The features of low operating current, wide and low operating voltage, high input impedance, and single supply operation are suitable for battery operated items.

■ FEATURES

- Operating Voltage (+2.7V ~ +32V)
- Low Operating Current (180 μ A typ. @V⁺=5V, each amplifier)
- Slew Rate (2.1V/ μ s typ.)
- Gain Bandwidth Product (1.8MHz typ.)
- Bipolar Technology
- Package Outline DIP8, DMP8, SSOP8, SIP8

■ PACKAGE OUTLINE



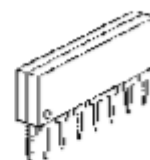
NJM2132D



NJM2132M

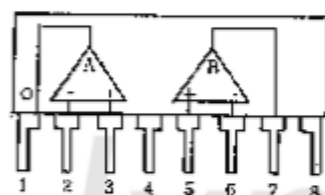


NJM2132V



NJM2132L

■ PIN CONFIGURATION

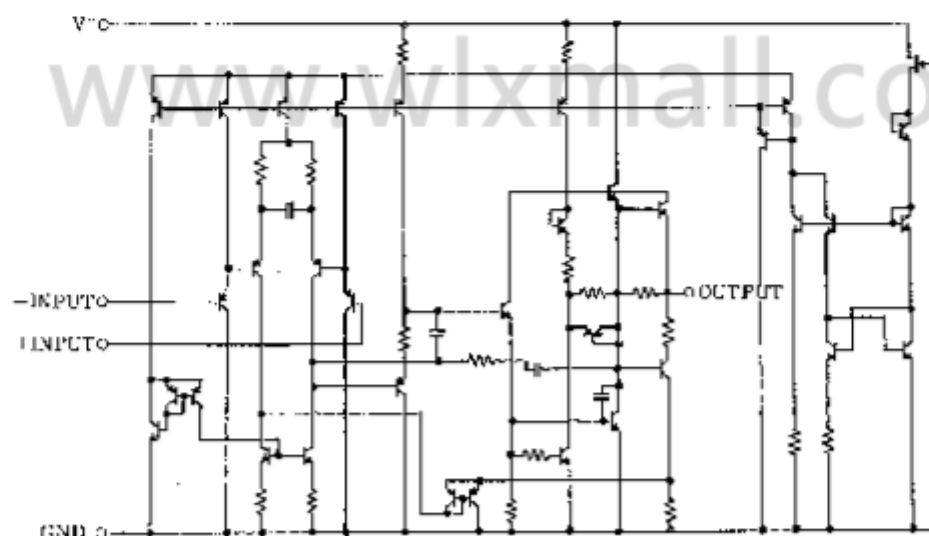
NJM2132D
NJM2132M
NJM2132V

NJM2132L

PIN FUNCTION

1. A OUTPUT
2. A - INPUT
3. A + INPUT
4. V⁻
5. B + INPUT
6. B - INPUT
7. B OUTPUT
8. V⁺

■ EQUIVALENT CIRCUIT (1/2 Shown)



■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ⁺	36	V
Differential Input Voltage	V _{ID}	±36	V
Input Voltage	V _{IC}	-0.3 ~ +36 (note)	V
Power Dissipation	P _D	(DIP-8) 500 (DMP-8) 300 (SSOP-8) 250 (SIP-8) 800	mW
Operating Temperature Range	T _{OPR}	-40 ~ +125	°C
Storage Temperature Range	T _{STG}	-50 ~ +125	°C

(note) When the supply voltage is less than +36V, the absolute maximum input voltage is equal to the supply voltage.

■ ELECTRICAL CHARACTERISTICS

(V⁺/V⁻=±15V, Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Operating Voltage	V ⁺	Single Supply	+2.7		+32	V
Input Offset Voltage	V _{IO}	R _S =0Ω V ⁺ =+15V, V ⁻ =0V, R _L =0Ω	-	2.0	4.5	mV
V _{IO} Drift	ΔV _{IO} /ΔT	R _S =0Ω	-	10	-	μV/°C
Input Offset Current	I _{IO}		-	5	20	nA
Input Bias Current	I _B		-	20	100	nA
Large Signal Voltage Gain	A _v	R _L ≥10kΩ, V _{IN} ≤10V	94	114	-	dB
Input Common Mode Voltage Range	V _{ICM}		-15 ~ +13.5 (MIN)			V
Common mode Rejection Ratio	CMR	R _S ≤10kΩ	80	90	-	dB
Supply Voltage Rejection Ratio	SVR	R _S =100Ω	80	100	-	dB
Maximum Output Voltage Swing	V _{OUT}	R _L =10kΩ	±13.6	±14.2	-	V
	+V _{CM} -V _{CM}	V ⁺ =+5V, V ⁻ =0V R _L =10kΩ	3.5 0.05	4.3 0.15	-	V
Operating Current	I _{CI}	R _L =∞ (all Amp.) V ⁺ =+5V, V ⁻ =0V	-	440 360	500 500	μA
Output Source Current	I _{OSOURCE}	V _{IN} =1V, V _{OUT} =0V	3.0	5.0	-	mA
Output Sink Current	I _{SINK}	V _{IN} =0V, V _{OUT} =1V	15	27	-	mA
Input Resistance	R _{IN}		-	300	-	MΩ
Input Capacitance	C _I		-	0.8	-	pF
Close Loop Output Impedance	Z _{OUT}	f=1.0MHz	-	100	-	Ω
Equivalent Input Noise Voltage	e _n	R _S =100Ω, f=1kHz	-	32	-	nV/√Hz
Slew Rate	SR	R _L =10kΩ	-	2.1	-	V/μs
Gain Bandwidth Product	GB	f=100kHz	-	1.8	-	MHz
Power Bandwidth	BW _p	A _v =+1.0, R _L =10kΩ V _{IN} =20V _{p-p} , THD=5%	-	35	-	kHz
Phase Margin	φ _M	R _L =10kΩ	-	60	-	deg.
		R _L =10kΩ, C _L =100pF	-	45	-	
Amplitude Margin	A _M	R _L =10kΩ	-	15	-	dB
		R _L =10kΩ, C _L =100pF	-	5.0	-	
Total Harmonic Distortion	THD	A _v =+1.0, R _L =10kΩ f=10kHz, 2≤V _{IN} ≤20V _{p-p}	-	0.03	-	%
Channel Separation	CS	f=10kHz, Input Refer 1	-	120	-	dB

备忘录



[警告] 本手册的规格是唯一的，
数据手册中的任何应用电路都是
仅供参考，不作为保证或许可。
对于本手册中的任何应用电路，
请仔细阅读本手册中的说明。