

Advanced Automotive EEPROM

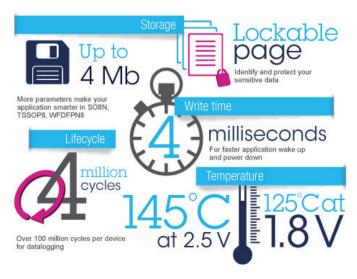


Introduction to Advanced Automotive EEPROM

Serial EEPROM is the most flexible type of non-volatile memory. When many parameters, heavy cycling requirements, safe data retention and high temperature mission profiles are required, EEPROM is the ideal product for high-quality and flexible parameter storage.

For the past 15 years, ST's automotive serial EEPROM has been ranked #1 offering a wide portfolio covering automotive needs and requirements. ST's Advanced Automotive series is designed to answer new automotive trends while providing increased robustness with AEC-Q100 Grade 0 qualification, PPAP Level 3 compliancy, zero-defect built-in quality and embedded Error Correction Code for safe and long-lasting data retention.

Three industry standard serial buses are supported: I2C, SPI and Microwire.



Thanks to an advanced proprietary manufacturing process, ST's Advanced Automotive series proposes an enlarged portfolio and offers a complete range of densities from 1 Kbit to 4 Mbits:

- Available in standard SO8N, TSSOP8 and small leadless DFN8 packages
- Temperature trends with 125 °C and 145 °C in-package solutions
- Operating from 5 V down to 1.8 V for compatibility with advanced controllers used in ADAS applications
- Ensuring fast application wake-up with 20 MHz for SPI and 1 MHz for I2C as well as data storage in 4 ms
- Supporting datalogging and event recording with 4 million E/W cycles at 25 °C (per byte) as well as over 100 million cycles per device
- Offering best-in-class retention times of 100 years at 25 °C and 50 years at 125 °C

Advanced Automotive series also offer an embedded lockable identification page to identify the EEPROM device and safely store the most sensitive parameters. With a specific set of instructions, data such as traceability, serial number, as well as unique ID can be stored and locked.

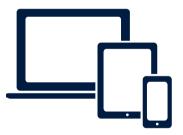
IBIS and Verilog models are available in the CAD resources of EEPROM webpages.

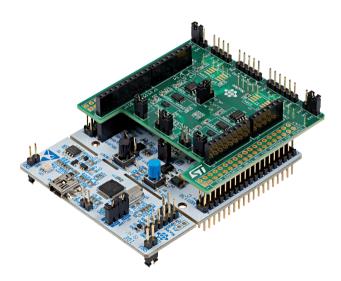












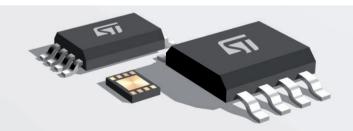
Visit st.com to discover our X-Nucleo-EEPRMA2 shield for fast prototyping.

Package options

Name	Package	Overall width (max)	Overall length (max)	Projected PCB area (mm²)	Overall height (max)	Pitch	Weight (mg)	Number of pins/ balls	Comments
S08N	First	5	6.2	31	1.75	1.27	80	8	
TSS0P8	597	3.1	6.6	20	1.2	0.65	34	8	
DFN8		2.1	3.1	6.5	0.8	0.5	22	8	Leadless package

All dimensions in mm

3D package models are available in the CAD resources of EEPROM webpages.



Automotive EEPROM Portfolio

SPI
All products are AEC-Q100 qualified and PPAP Level 3-compliant

Part number	Charage consoits (Vhit)	Townsecture rouge (90)	Dower comply renge (II)	Max. clock frequency	Package options ²			
rari number	Storage capacity (Kbit)	Temperature range (°C)	Power supply range (V)	(MHz) ¹	S08N	TSSOP8	DFN8	
M95020-A125	2	-40 to 125	1.7 to 5.5	20	Х	Х	X	
M95040-A125	4	-40 to 125	1.7 to 5.5	20	Χ	Х	Х	
M95080-A125	8	-40 to 125	1.7 to 5.5	20	Х	Х	Х	
M95160-A125	16	-40 to 125	1.7 to 5.5	20	Х	Х	Х	
M95320-A125	32	-40 to 125	1.7 to 5.5	20	Х	Х	Х	
M95640-A125	64	-40 to 125	1.7 to 5.5	20	Х	Х	Х	
M95128-A125	128	-40 to 125	1.7 to 5.5	20	Х	Х	Х	
M95256-A125	256	-40 to 125	1.7 to 5.5	20	Х	Х	Х	
M95512-A125	512	-40 to 125	1.7 to 5.5	16	Х	Х	Х	
M95M01-A125	1024	-40 to 125	2.5 to 5.5	16	Х	Х		
M95M02-A125	2048	-40 to 125	2.5 to 5.5	5	Х			
M95M04-A125	4096	-40 to 125	2.9 to 5.5	10	Х	Х		

SPI (CONT'D)

All products are AEC-Q100 qualified and PPAP Level 3-compliant

Part number	Storage capacity (Kbit)	Temperature range (°C)	Power supply range (V)	Max. clock frequency	Package options ²			
rait ilullibei	Storage capacity (KDIL)	Temperature range (6)	rower supply ralige (v)	(MHz) ¹	S08N	TSSOP8	DFN8	
M95040-A145	4	-40 to 145	2.5 to 5.5	10		Х		
M95080-A145	8	-40 to 145	2.5 to 5.5	10		Х		
M95160-A145	16	-40 to 145	2.5 to 5.5	10		Х		
M95320-A145	32	-40 to 145	2.5 to 5.5	10		Х		
M95640-A145	64	-40 to 145	2.5 to 5.5	10		Х		
M95128-A145	128	-40 to 145	2.5 to 5.5	10	Х	Х		
M95256-A145	256	-40 to 145	2.5 to 5.5	10		Х		
M95512-A145	512	-40 to 145	2.5 to 5.5	10		Х		
M95M01-A145	1024	-40 to 145	2.5 to 5.5	10		Х		
M95M04-A145	4096	-40 to 145	2.9 to 5.5	10		Х		

Notes:

- 1. The clock frequency value depends on the Vcc applied. The values given here are based on a 5 V supply.
- 2. For more options contact your nearest ST sales office or online support at st.com.

All products benefit from a lockable identification page and support datalogging and event recording with 4 million E/W cycles at 25 °C, 1.2 million at 85 °C and 0.4 million at 145 °C (per byte) as well as over 100 million cycles per device.

I2C
All products are AEC-Q100 qualified and PPAP Level 3-compliant

Part number	Storage capacity (Kbit)	Temperature range (°C)	Power supply range (V)	Max. clock frequency	Package options			
rait ilullibei	Storage capacity (Kult)	remperature range (6)	rower supply ralige (v)	(MHz)	S08N	TSS0P8	DFN8	
M24C02-A125	2	-40 to 125	1.7 to 5.5	1	Х	Х	Х	
M24C04-A125	4	-40 to 125	1.7 to 5.5	1	Х	Х	Х	
M24C08-A125	8	-40 to 125	1.7 to 5.5	1	Х	Х	Х	
M24C16-A125	16	-40 to 125	1.7 to 5.5	1	Х	Х	Х	
M24C32-A125	32	-40 to 125	1.7 to 5.5	1	Х	Х	Х	
M24C64-A125	64	-40 to 125	1.7 to 5.5	1	Х	Х	Х	
M24128-A125	128	-40 to 125	1.7 to 5.5	1	Х	Х	Х	
M24256-A125	256	-40 to 125	1.7 to 5.5	1	Х	Х	Х	
M24512-A125	512	-40 to 125	1.7 to 5.5	1	Х	Х	Х	
M24M01-A125	1024	-40 to 125	2.5 to 5.5	1	Х	Х		
M24M02-A125	2048	-40 to 125	2.5 to 5.5	1	Х			

All products benefit from a lockable identification page and support datalogging and event recording with 4 million E/W cycles at 25 °C, 1.2 million at 85 °C and 0.4 million at 145 °C (per byte) as well as over 100 million cycles per device.

MICROWIRE

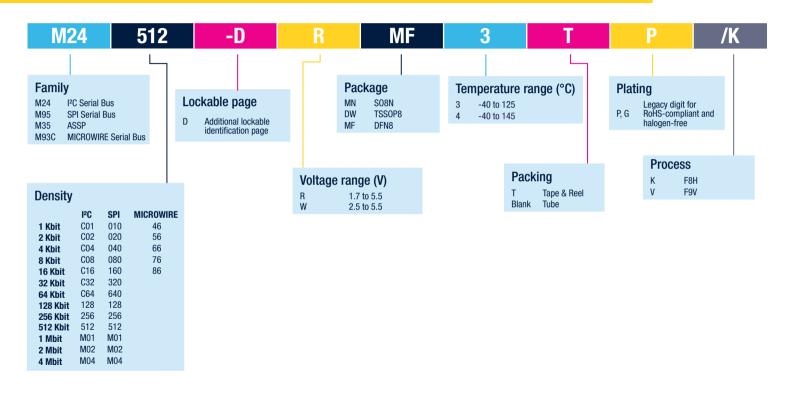
All products are AEC-Q100 qualified and PPAP Level 3-compliant

Root part number	Storage capacity (Kbit)	Temperature range (°C)	Power supply range (V)	Clock frequency	Package	options	Specific features
				max (MHz)	SO8N	TSS0P8	Specific leatures
M93C46-A125	1	-40 to 125	1.8 to 5.5	2	X	Χ	8- or 16-bit organization
M93C56-A125	2	-40 to 125	1.8 to 5.5	2	X	Χ	8- or 16-bit organization
M93C66-A125	4	-40 to 125	1.8 to 5.5	2	Х	X	8- or 16-bit organization
M93C76-A125	8	-40 to 125	1.8 to 5.5	2	Х	Х	8- or 16-bit organization
M93C86-A125	16	-40 to 125	1.8 to 5.5	2	X	Χ	8- or 16-bit organization

All products support datalogging and event recording with 4 million E/W cycles at 25 °C, 1.2 million at 85 °C and 0.4 million at 145 °C (per byte) as well as over 100 million cycles per device.

Please check for up-to-date information on our website www.st.com/advautoeeeprom

Ordering Information



Notes

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